Display after switching on

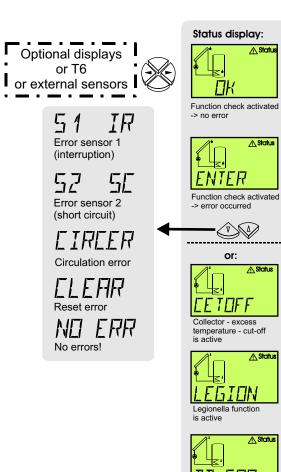


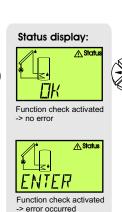
Activate input

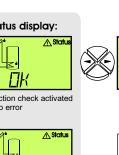
Confirm entry

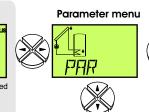
Change program number

Parameter menu UVR63 vers. 2.6









[[]]E 32

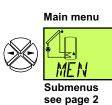
Activate input

Confirm entry

diff 1 Difference

switch-on threshold

Change code



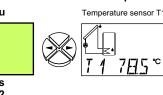
The version indicates

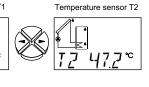
diff 1 Difference

switch-off threshold

the development status

of the control engineering





LO OFF

Activate input

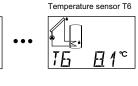
Select

LO 1X2

Output 1 is crossed with output 2

Confirm entry

crossing out variant



PA OFF

Activate input

Output 1 is favoured over output 2

Confirm entry

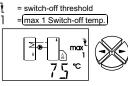
PA

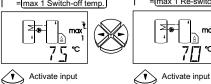
Select or allocate **priority** for the participating output

for the participating outputs

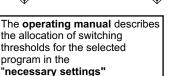


Volume flow











= switch-on threshold

Change value

NO FER

PB FER

|A3 + A2

A3 switches with A1

A3 activated (as time switch output)

=(max 1 Re-switch-on temp.



= switch-on threshold

= min 1 Switch-on temp.

mln₁

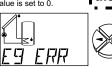
min = Min limit

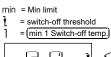
Activate input

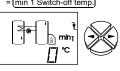
Confirm entry

Change value



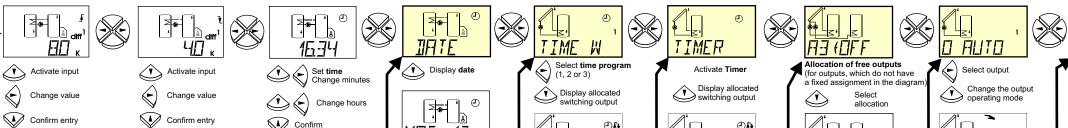












OPA

Activate input

Select participating outputs "and" or "or

(e.a. OPA 1)

∳₽©





All display segments

displayed when the

device is started up.

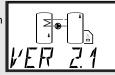
are momentarily

confirm

max min

Drain-back

After startup the type designation and the version number are again change (flashing text = ready for entering) shown in the display. This gives information about the device intelligence

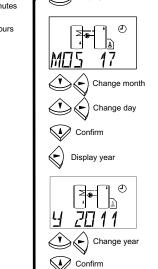


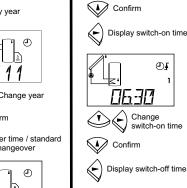
The factory setting is loaded by pressing button 🖎 during plugging in. The following appears on the display:

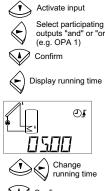
(Load factory setting)

(important for support queries).

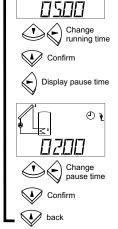


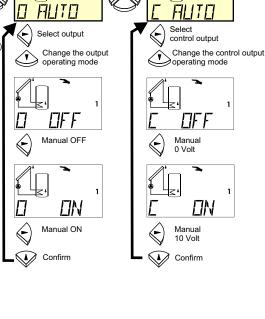




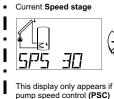


OPA







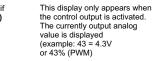


is activated. The currently

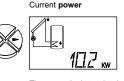
output speed stage is

indicated (example 30).

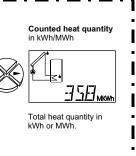


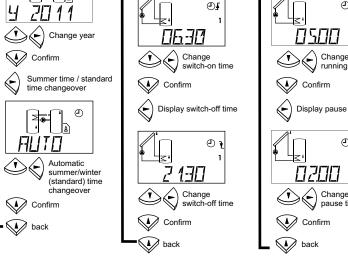


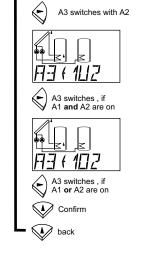
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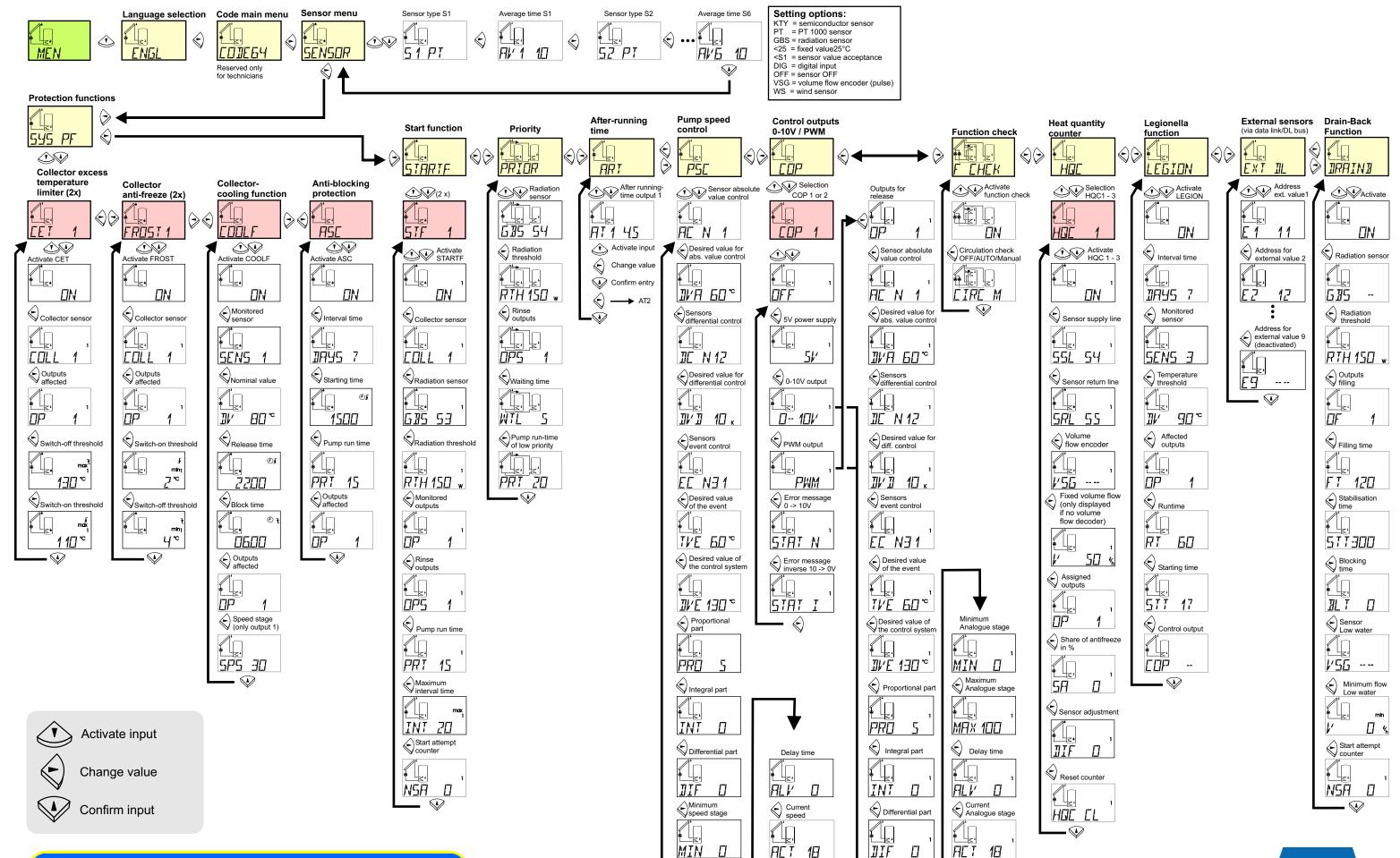












Maximum speed stage

MFIX

Output mode

Setting of test speed

151

Setting of test analogue stage

Main menu UVR63

